# Coin Battery Charger IC for Solar-Powered Systems CN3130

## **General Description:**

The CN3130 is a complete constant-current /constant voltage linear charger for coin batteries. The device contains an on-chip power MOSFET and eliminates the need for the external sense resistor and blocking diode. An on-chip adaptive block can adjust charging current automatically based on the output capability of input power supply, so CN3130 is ideally suited for solar powered system. Thermal feedback regulates the charge current to limit the die temperature during high power operation or high ambient temperature. The regulation voltage is internally fixed at 3.3V with 1% accuracy, it can also be adjusted upwards with an external resistor. The charge current can be set externally with a single resistor. When the input supply is removed, the CN3130 automatically enters a low power sleep mode, dropping the battery drain current to less than 3uA. Other features include undervoltage lockout, automatic recharge and charging indicator. The CN3130 is available in 6 pin SOT23 package.

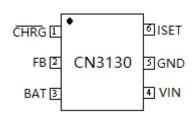
## **Applications:**

- Wearable Equipment
- Smart Home
- Medical Equipment, Calculator
- Electric Meter, Water Meter and Gas Meter
- Measurement Equipment and Sensor

#### **Features:**

- Automatic charge current adjustment based on the output capability of input power supply
- Suitable for Solar-Powered System
- On-chip Power MOSFET
- Suitable for Charging Manganese Lithium , LiFePO4 and Lithium Coin Battery
- No external Blocking Diode or Current Sense Resistors Required
- Preset 3.3V regulation voltage with 1% accuracy, upwards adjustable with a resistor
- Precharge Conditioning for Reviving Deeply Discharged Cells and Minimizing Heat Dissipation During Initial Stage of Charge
- Constant Charge Current from 10mA to 300mA
- Constant-Current/Constant-Voltage Operation with Thermal Regulation to Maximize Charge Rate Without Risk of Overheating
- Automatic Low-Power Sleep Mode When Input Supply Voltage is Removed
- Status Indication for LEDs or uP Interface
- C/10 Charge Termination
- Automatic Recharge
- Available in SOT23-6 Package
- Pb-free, rohs-Compliant and Halogen Free

## Pin Assignment



## **Typical Application Circuit**

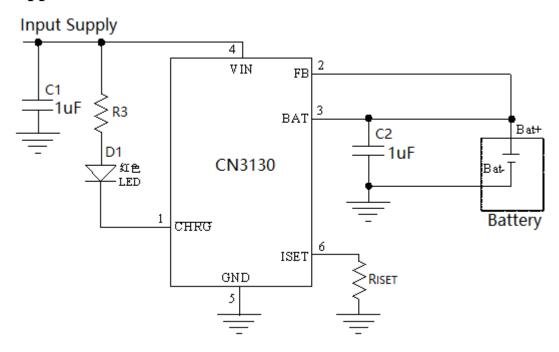


Figure 1 Typical Application Circuit(Constant Voltage Level 3.3V)

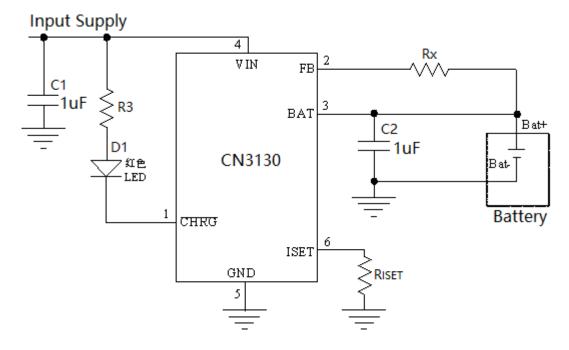


Figure 2 Application Circuit(Adjust Constant Voltage Level with Rx)

In Figure 2, the BAT pin's voltage in constant voltage mode is given by the following equation:

$$V_{REG}=3.3+3.707\times10^{-6}\times Rx$$
 Where,  $V_{REG}$  is in volt Rx is in ohm

Note: The accuracy and temperature coefficient of the regulation voltage may be deteriorated due to the process variation and different characteristics between internal resistor and the external resistor.

# **Ordering Information:**

Part No.	Package	Shipping	<b>Operating Temperature Range</b>
CN3130	SOT23-6	Tape and Reel, 3000/Reel	-40°C to +85°C

## **Block Diagram**

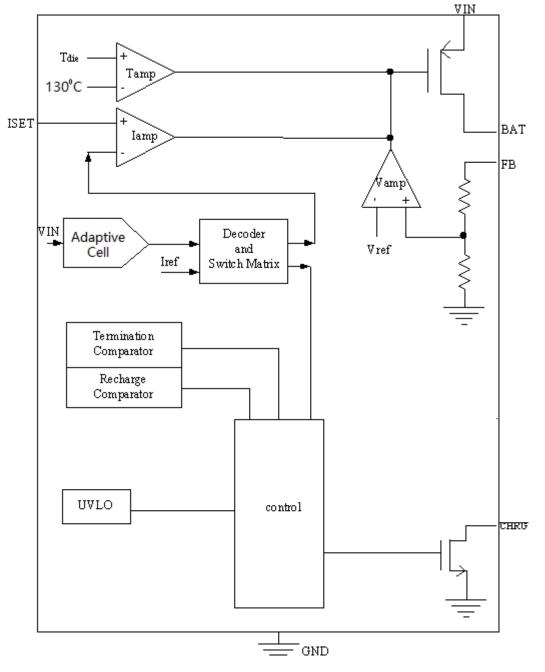


Figure 3 Block Diagram

# **Pin Description**

Pin No.	Name	Function Description		
		Open Drain Charge Status Output. When the battery is being charged, the		
1	CHRG	CHRG pin is pulled low by an internal switch, otherwise CHRG pin is in high		
		impedance state.		
		Battery Voltage Sense Input. This Pin can sense the battery voltage; Also the		
2	FB	regulation voltage in constant voltage mode can be adjusted by connecting an		
		external resistor between FB pin and BAT pin.		
		Battery Connection Pin. Connect the positive terminal of the battery to BAT		
3	BAT	pin. BAT pin draws less than 3uA current in sleep mode. BAT pin provides		
		charge current to the battery and provides regulation voltage.		
		<b>Positive Input Supply Voltage.</b> V <sub>IN</sub> is the power supply to the internal circuit.		
4	VIN	When V <sub>IN</sub> drops to within 10mv of the BAT pin voltage, CN3130 enters low		
		power sleep mode, dropping BAT pin's current to less than 3uA.		
5	GND	Ground Terminal (Ground).		
		Constant Charge Current Setting and Charge Current Monitor Pin. The		
6	ISET	charge current is set by connecting a resistor R <sub>ISET</sub> from this pin to GND.		
		Under the precharge mode, the ISET pin's voltage is regulated to 0.36V. Under		
		the constant current mode, the ISET pin's voltage is regulated to 1.205V. In all		
		modes during charging, the voltage on ISET pin can be used to measure the		
		charge current as follows:		
		$I_{CH} = (V_{ISET} / R_{ISET}) \times 986$		

# **Absolute Maximum Ratings**

All Terminal Voltage0.3V to 6.5V	Maximum Junction Temperature150 $^{\circ}$ C
BAT Short-Circuit DurationContinuous	Operating Temperature−40°C to 85°C
Storage Temperature $-65^{\circ}\mathrm{C}$ to $150^{\circ}\mathrm{C}$	Thermal Resistance (SOT23-6)TBD
Lead Temperature(Soldering, 10s)260°C	

Stresses beyond those listed under 'Absolute Maximum Ratings' may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions above those indicated in the operational sections of the specifications is not implied. Exposure to Absolute Maximum Rating Conditions for extended periods may affect device reliability.

## **Electrical Characteristics**

(VIN=5V,  $T_A$ =-40°C to 85°C, Typical Values are measured at  $T_A$ =25°C,unless otherwise noted)

Parameters	Symbol	Test Conditions	Min	Тур	Max	Unit	
Input Supply Voltage	VIN		3.9		6	V	
Operating Current	I <sub>VIN</sub>	Charge Termination Mode	350	500	660	uA	
Undervoltage Lockout	Vuvlo	VIN falling		2.4	2.65	V	
Undervoltage Lockout Hysteresis	Huvlo			0.12		V	
Soft Start Time	$t_{SS}$		100	160	220	uS	
Regulation Voltage	$V_{REG}$	Constant Voltage Mode	3.27	3.3	3.33	V	
	$I_{CC}$	$R_{ISET}=11.8K, V_{BAT}=3.6V$	90	100	110	mA	
DAT nin Cumant	$I_{PRE}$	$R_{ISET}=11.8K, V_{BAT}=2V$	22	30	38		
BAT pin Current	$I_{SDBY}$	V <sub>BAT</sub> =V <sub>REG</sub> , standby mode	1.8	3.7	7	uA	
	$I_{SLP}$	VIN=0V, sleep mode			3		
Precharge Threshold							
Precharge Threshold	$V_{PRE}$	Voltage at BAT pin rising	67	70	73	$%V_{REG}$	
Precharge Threshold	11			4.0		0/37	
Hysteresis	$H_{PRE}$			4.2	$ $ % $V_{RI}$	$%V_{REG}$	
Charge Termination Th	reshold						
Charge Termination	Vterm	Vterm Measure voltage at ISET pin 96	06	96 120	144	mV	
Threshold			90				
Recharge Threshold							
Recharge Threshold	I <sub>RECH</sub>	Charge Current Rises		30%		$%I_{CC}$	
Sleep Mode							
Class Made Threehold	<b>X</b> 7	V <sub>IN</sub> from high to low, measures	15	32	50	mV	
Sleep Mode Threshold	$V_{SLP}$	the voltage $(V_{IN}-V_{BAT})$					
Sleep mode Release	V <sub>SLPR</sub>	V <sub>IN</sub> from low to high, measures	57	80	120	mV	
Threshold		the voltage $(V_{IN}-V_{BAT})$					
FB Pin							
FB Bias Current 1	IFB1	VFB=3.6V, Charge Mode	1.8	3	5	$\mu$ <b>A</b>	
FB Bias Current 2	IFB2	VIN <vuvlo or="" td="" vin<vbat<=""><td></td><td></td><td>1</td><td><math>\mu</math><b>A</b></td></vuvlo>			1	$\mu$ <b>A</b>	
ISET Pin							
ICET Dia Valea	V <sub>ISET</sub>	Precharge mode		0.36		<b>T</b> 7	
ISET Pin Voltage		Constant current mode		1.205		V	
CHRG Pin							
CHRG Sink Current	I <sub>CHRG</sub>	V <sub>CHRG</sub> =0.3V, Charge status		10		mA	
CHRG Leakage Current		V <sub>CHRG</sub> =6V, termination mode			1	uA	

## **Detailed Description**

The CN3130 is a linear battery charger IC designed primarily for charging coin batteries. Featuring an internal P-channel power MOSFET, the charger uses a constant-current/constant-voltage to charge the batteries. Continuous charge current can be programmed from 10mA to 300mA with an external resistor. No blocking diode or sense resistor is required. The on-chip adaptive cell can adjust charging current automatically based on the output capability of input power supply, so CN3130 is ideally suited for the solar-powered systems, or the applications that need to charge the battery with an input power supply whose output capability is limited. The open-drain output CHRG indicates the charger's status. The internal thermal regulation circuit reduces the programmed charge current if the die temperature attempts to rise above a preset value of approximately 130°C. This feature protects the CN3130 from excessive temperature, and allows the user to push the limits of the power handling capability of a given circuit board without risk of damaging the CN3130 or the external components. Another benefit of adopting thermal regulation is that charge current can be set according to typical, not worst-case, ambient temperatures for a given application with the assurance that the charger will automatically reduce the current in worst-case conditions.

The regulation voltage is internally fixed at 3.3V with 1% accuracy, it can also be adjusted upwards with an external resistor, which make the CN3130 very suitable for charging Manganese-Lithium coin/button battery, LiFePO4 coin/button battery and Lithium ion coin/button battery.

The charge cycle begins when the voltage at the VIN pin rises above the UVLO level, a current set resistor is connected from the ISET pin to ground. The  $\overline{\text{CHRG}}$  pin outputs a logic low to indicate that the charge cycle is ongoing. At the beginning of the charge cycle, if the voltage at FB pin is below 70% of the regulation voltage, the charger is in precharge mode to bring the cell voltage up to a safe level for charging. The charger goes into the fast charge constant-current mode once the voltage on the FB pin rises above 70% of the regulation voltage. In constant current mode, the charge current is set by  $R_{\text{ISET}}$ . When the battery approaches the regulation voltage, the charge current begins to decrease as the CN3130 enters the constant-voltage mode. When the current drops to charge termination threshold, the charge cycle is terminated and  $\overline{\text{CHRG}}$  pin is in high impedance state. The charge termination threshold is 10% of the current in constant current mode. To restart the charge cycle, just remove the input voltage and reapply it. The charge cycle can also be automatically restarted if the charge current rises above the recharge threshold. The on-chip reference voltage, error amplifier and the resistor divider provide regulation voltage with 1% accuracy which can meet the requirement of batteries. When the input voltage is not present, the charger goes into a sleep mode, dropping battery drain current to less than 3uA. This greatly reduces the current drain on the battery and increases the standby time.

The charging profile is shown in Figure 4.

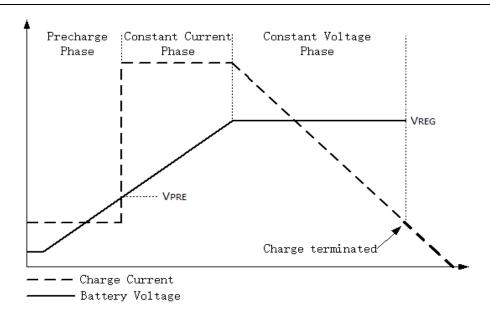


Figure 4 Charging Profile

## **Application Information**

#### **Undervoltage Lockout (UVLO)**

An internal undervoltage lockout circuit monitors the input voltage and keeps the charger in shutdown mode until VIN rises above the undervoltage lockout voltage plus the hysteresis. The UVLO circuit has a built-in hysteresis of 0.12V.

#### Sleep mode

There is an on-chip sleep comparator. The comparator keeps the charger in sleep mode if VIN falls below sleep mode threshold. Once in sleep mode, the charger will not come out of sleep mode until VIN rises above the sleep mode release threshold.

#### Precharge mode

At the beginning of a charge cycle, if the battery voltage is below 70% of the regulation voltage, the charger goes into precharge mode, and the charge current is 30% of fast charge current in constant current mode.

#### Charging Current limited by the Output capability of Input Power Supply

If the output capability of input power supply is less than the charging current set by the resistor at ISET pin, then the on-chip adaptive cell will begin to function to adjust the charging current based on the output capability of input power supply. In this case, the charging current may be less than the value set by the resistor at ISET pin, but it is maximized to the output capability of input power supply on the condition that the input voltage at VIN pin is no less than 3.9V, which is the minimum operating voltage of CN3130. So the charging current can be set according to the maximum output capability of input power supply, not the worst case.

#### Adjusting the regulation voltage in constant voltage mode

The regulation voltage in constant voltage mode can be adjusted by an external resistor connecting between FB pin and BAT pin as shown in Figure 5.

In Figure 5, the regulation voltage in constant voltage mode will be given by the following equation:

$$V_{REG} = 3.3 + 3.707 \times 10^{-6} \times Rx$$

Where.

 $V_{REG}$  is in volt Rx is in ohm

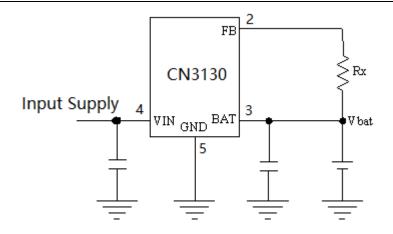


Figure 5 Adjusting Regulation Voltage in Constant Voltage Mode

#### **Setting Charge Current**

The formula for the battery charge current in constant current mode is:

$$I_{CH} = 1188V / R_{ISET}$$

Where:

I<sub>CH</sub> is the charge current in ampere

 $R_{\mbox{\scriptsize ISET}}$  is the total resistance from the ISET pin to ground in ohm

For example, if 100mA charge current is required, calculate:

$$R_{ISET}=1188V/0.1A=11.88k\,\Omega$$

For best stability over temperature and time, 1% metal film resistors are recommended. If the charger is in constant-temperature or constant voltage mode, the charge current can be monitored by measuring the ISET pin voltage, and the charge current is calculated as the following equation:

$$I_{CH} = (V_{ISET} / R_{ISET}) \times 986$$

#### **Combine Two Power Inputs**

Although the CN3130 allows charging from a solar power supply, a wall adapter or a USB port can also be used to charge LiFePO4 batteries. Figure 6 shows an example of how to combine 2 power inputs. A P-channel MOSFET, M1, is used to prevent back conducting into the  $2^{nd}$  power supply when the  $1^{st}$  power supply is present and Schottky diode, D1, is used to prevent  $2^{nd}$  power supply loss through the  $1k \Omega$  pull-down resistor.

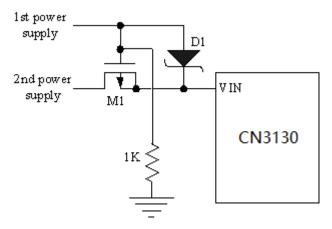


Figure 6 Combining 2 Input Power Supply

#### Recharge

After a charge cycle has terminated, if the charge current rises above 30% of the constant charge current, a new charge cycle will begin automatically.

#### Constant-Current/Constant-Voltage/Constant-Temperature

The CN3130 use a unique architecture to charge a battery in a constant-current, constant-voltage, constant temperature fashion as shown in Figure 3. Amplifiers Iamp, Vamp, and Tamp are used in three separate feedback loops to force the charger into constant-current, constant-voltage, or constant-temperature mode, respectively. In constant current mode the charge current delivered to the battery equal to  $1188V/R_{ISET}$ . If the power dissipation of the CN3130 results in the junction temperature approaching  $130^{\circ}C$ , the amplifier Tamp will begin decreasing the charge current to limit the die temperature to approximately  $130^{\circ}C$ . As the battery voltage rises, the CN3130 either returns to constant-current mode or it enters constant voltage mode straight from constant-temperature mode.

#### **Open-Drain Status Output**

The CN3130 has 1 open-drain status output CHRG. CHRG is pulled low when the charger is in charging status, otherwise CHRG becomes high impedance.

The open drain status output should be tied to ground if it is not used.

#### **VIN Bypass Capacitor**

Many types of capacitors can be used for input bypassing(C1 in Figure 1 and 2), Generally, a 1uF ceramic capacitor, placed in close proximity to VIN and GND pins, works well. In some applications depending on the power supply characteristics and cable length, it may be necessary to increase the capacitor's value. If the ceramic capacitor is used as the input supply bypassing purpose, a voltage spike may be created when the input voltage is applied to the CN3130 via a cable. If the cable is a bit long, the circuit shown in Figure 7 or a TVS diode from VIN pin to GND should be considered to use to prevent the CN3130 from being damaged by the voltage spike.

For the consideration of the bypass capacitor, please refer to the Application Note AN102 from our website.

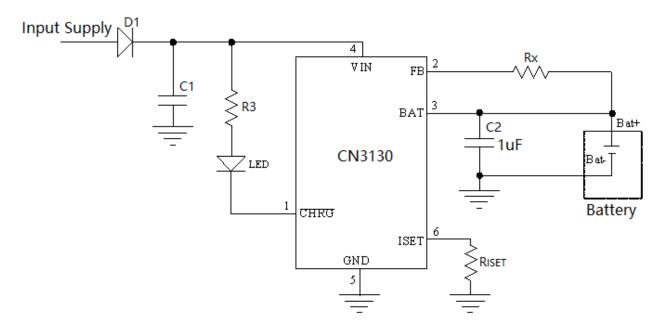


Figure 7 Adding Diode D1 to Suppress Voltage Spike

#### **Stability**

A capacitor from BAT pin to GND is required to stabilize the feedback loop, generally the feedback loop is stable with an 1uF to 22uF ceramic capacitor. If electrolytic capacitor is used, the capacitance can be as high as 100uF

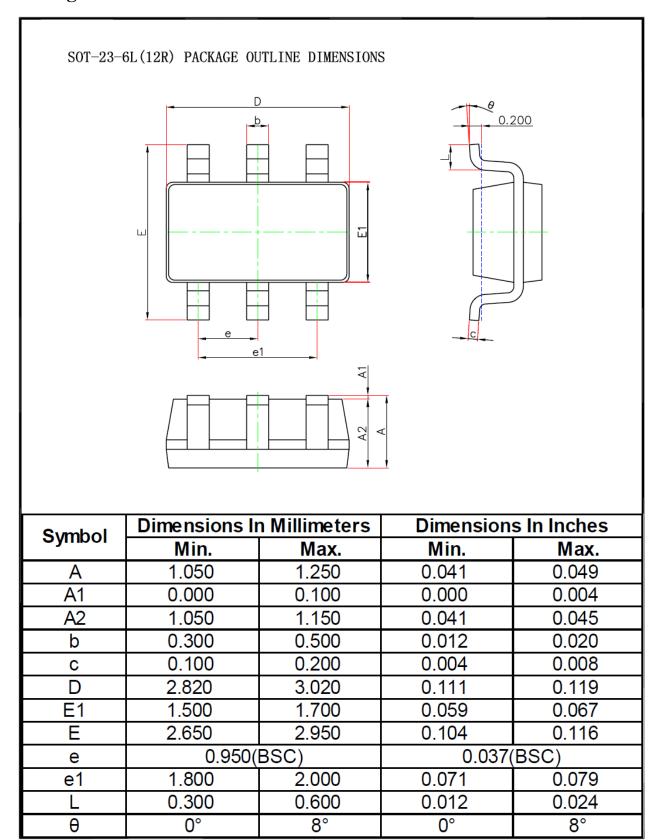
In constant current mode, the stability is also affected by the impedance at the ISET pin . With no additional capacitance on the ISET pin, the loop is stable with current set resistors values as high as  $120 \text{K}\ \Omega$ . However, additional capacitance on ISET pin reduces the maximum allowed current set resistor. The pole frequency at ISET pin should be kept above 100KHz. Therefore, if ISET pin is loaded with a capacitance C, the following equation should be used to calculate the maximum resistance value for  $R_{\text{ISET}}$ :

$$R_{ISET} < 1 / (6.28 \times 1 \times 10^5 \times C)$$

#### **Board Layout Considerations**

- 1.  $R_{ISET}$  at ISET pin should be as close to CN3130 as possible, also the parasitic capacitance at ISET pin should be kept as small as possible.
- 2. The capacitance at VIN pin and BAT pin should be as close to CN3130 as possible.
- 3. It is very important to use a good thermal PC board layout to maximize charging current. The thermal path for the heat generated by the IC is from the die to the copper lead frame through the package lead(especially the ground lead) to the PC board copper, the PC board copper is the heat sink. The footprint copper pads should be as wide as possible and expand out to larger copper areas to spread and dissipate the heat to the surrounding ambient. Feedthrough vias to inner or backside copper layers are also useful in improving the overall thermal performance of the charger. Other heat sources on the board, not related to the charger, must also be considered when designing a PC board layout because they will affect overall temperature rise and the maximum charge current.

## **Package Information**



Consonance does not assume any responsibility for use of any circuitry described. Consonance reserves the right to change the circuitry and specifications without notice at any time.