400mA Single-Cell Lithium Battery Charger IC CN3140

General Description:

The CN3140 is a complete constant-current /constant voltage linear charger for single cell Li-ion and Li Polymer batteries. The device contains an on-chip power MOSFET and eliminates the need for the external sense resistor and blocking diode. An on-chip adaptive cell can adjust charging current automatically based on the output capability of input power supply, so CN3140 is ideally suited for solar powered system. Thermal feedback regulates the charge current to limit the die temperature at about 132°C during high power operation or high ambient temperature. The regulation voltage is internally fixed at 4.2V with 1% accuracy. The charge current can be set externally with a single resistor, the minimum charge current can be set down to 3mA, which makes CN3140 very suitable for charging ultra-small capacity battery. When the input supply is removed, the CN3140 automatically enters a low power sleep mode, dropping the battery drain current to less than 3uA.

Other features include undervoltage lockout, automatic recharge, JEITA-compliant battery temperature monitoring, over charge current protection and charging indicator.

The CN3140 is available in 2mm*2mm 6-pin DFN package.

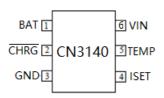
Applications:

- Medical Electronics
- Wearable Equipment
- Beauty and Grooming
- Fleet Management, Asset Tracking

Features:

- Input Supply Voltage Range: 4.5V to 6V
- Automatic charge current adjustment based on the output capability of input power supply
- Suitable for Solar-Powered System
- Regulation voltage: $4.2V \pm 1\%$
- Continuous Charge Current Up to 400mA
- Minimum Constant Charge Current down to 3mA
- Over Charge Current Protection in case Short Circuit at ISET Pin
- Precharge Conditioning for Reviving Deeply Discharged Cells and Minimizing Heat Dissipation During Initial Stage of Charge
- Constant-Current/Constant-Voltage Operation with Thermal Regulation to Maximize Charge Rate Without Risk of Overheating
- Automatic Low-Power Sleep Mode When Input Supply Voltage is Removed
- Status Indication for LEDs or uP Interface
- Automatic Charge Termination and Recharge
- JEITA-Compliant Battery Temperature Monitoring
- Available in 2mm*2mm DFN-6 Package
- Pb-free, rohs-Compliant and Halogen Free

Pin Assignment



Typical Application Circuit

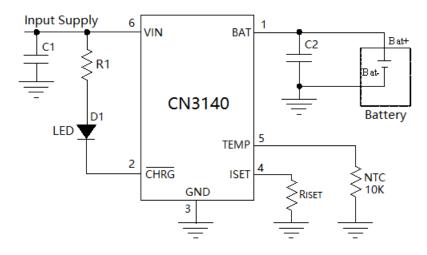


Figure 1 Typical Application Circuit

Ordering Information:

Part No.	Package	Top Marking	Shipping	Operating Temperature Range
CN3140	DFN-6	3140	Tape&Reel, 3K/Reel	-40°C to $+85^{\circ}\text{C}$

Block Diagram

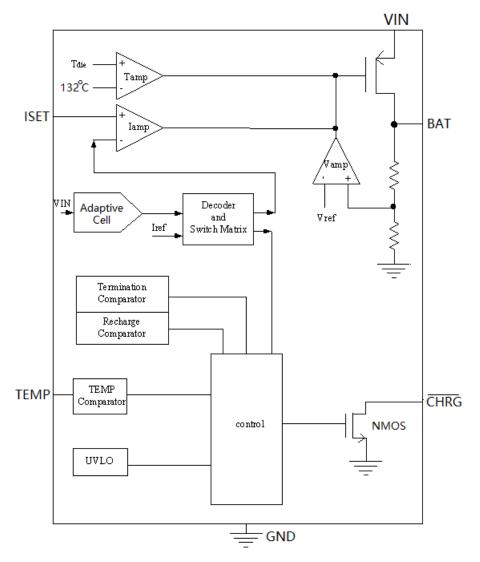


Figure 2 Block Diagram

Pin Description

Pin No.	Name	Function Description		
	BAT	Battery Positive Terminal Connection Pin. Connect the positive terminal of		
1		the battery to BAT pin. BAT pin draws less than 3uA current in sleep mode.		
		BAT pin provides charge current to the battery and provides regulation voltage.		
	CHRG	Open Drain Charge Status Output. When the battery is being charged, the		
2		CHRG pin outputs pulse of 2Hz frequency, the LED connected to CHRG pin		
2		will blink. In termination mode, CHRG pin is pulled low by an internal switch.		
		In other cases CHRG pin is in high impedance state.		
3	GND	Ground Terminal (Ground).		
		Constant Charge Current Setting and Charge Current Monitor Pin. The		
		charge current is set by connecting a resistor R _{ISET} from this pin to GND.		
		When in precharge mode, the ISET pin's voltage is regulated to 0.4V. When in		
4	ISET	constant charge current mode, the ISET pin's voltage is regulated to 1.205V. In		
		all modes during charging, the voltage on ISET pin can be used to measure the		
		charge current as follows:		
		$I_{CH} = (V_{ISET} / R_{ISET}) \times 417$		
		Battery Temperature Monitoring Input. Connecting a 10K ohm NTC		
	is too hot or too cold, charging is suspended. • If TEMP's voltage is between 0.1V and 0.135V, which mean	thermistor between TEMP pin and GND.		
		• If TEMP pin's voltage is below 0.1V or above 0.85V, which means battery		
		is too hot or too cold, charging is suspended.		
		• If TEMP's voltage is between 0.1V and 0.135V, which means battery is		
		warm, charge current is reduced to 50% of full-scale current set by resistor		
5	TEMP	at ISET pin and regulation voltage is reduced to 4.085V.		
3	I LIVII	• If TEMP's voltage is between 0.135V and 0.55V, CN3140 functions		
		normally.		
		• If TEMP's voltage is between 0.55V and 0.85V, which means battery is		
		cool, charge current is reduced to 25% of full-scale current set by resistor		
		at ISET pin.		
		If battery temperature monitoring function is not needed, connect a fixed 10K		
		ohm resistor from TEMP pin to GND.		
		Positive Input Supply Voltage. VIN is the power supply to the internal circuit.		
6	VIN	When VIN drops to within 10mv of the BAT pin voltage, CN3140 enters low		
		power sleep mode, dropping BAT pin's current to less than 3uA.		

Absolute Maximum Ratings

All Terminal Voltage0.3V to 6.5V	Maximum Junction Temperature150℃
BAT Short-Circuit DurationContinuous	Operating Temperature−40°C to 85°C
Storage Temperature65 $^{\circ}$ C to 150 $^{\circ}$ C	Thermal Resistance (DFN-6)TBD
Lead Temperature(Soldering, 10s)260°C	

Stresses beyond those listed under 'Absolute Maximum Ratings' may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions above those indicated in the operational sections of the specifications is not implied. Exposure to Absolute Maximum Rating Conditions for extended periods may affect device reliability.

Electrical Characteristics

(VIN=5V, T_A =-40°C to 85°C, Typical Values are measured at T_A =25°C,unless otherwise noted)

Parameters	Symbol	Test Conditions	Min	Тур	Max	Unit
Input Supply Voltage	VIN		4.5		6	V
Operating Current	I _{VIN}	Charge Termination Mode	280	390	500	uA
Undervoltage Lockout	Vuvlo	VIN falling		2.4	2.65	V
Undervoltage Lockout Hysteresis	Huvlo			0.12		V
Soft Start Time	t_{SS}		100	160	220	uS
Regulation Voltage	V_{REG}	Constant Voltage Mode	4.158	4.2	4.242	V
	I_{CC1}	R _{ISET} =1.25K, V _{BAT} =3.6V	340	400	460	
	I _{PRE1}	R _{ISET} =1.25K, V _{BAT} =2.4V	101	133	165	mA
BAT pin Current	I _{SRT1}	$R_{ISET}=1.25K$, $V_{BAT}=0V$	30	44	60	
	I_{SDBY}	V _{BAT} =4.2V, termination mode	1.8	3.7	7	
	I_{SLP}	VIN=0V, sleep mode			3	uA
Battery Short-circuit						
Short-circuit Threshold	V_{SRT}	Battery voltage falls	0.58	0.89	1.2	V
Short-circuit Current	I_{SRT}	$V_{BAT} < V_{SRT}$	6	11	16	%Icc
Precharge (Trickle Cha	rge)					
Precharge Threshold	V_{PRE}	Voltage at BAT pin rising	2.67	2.8	2.93	V
Precharge Threshold Hysteresis	H _{PRE}			68		mV
Precharge Current	I _{PRE}	$V_{SRT} < V_{BAT} < V_{PRE}$	25	33.3	42	%Icc
Charge Termination Tl	hreshold	,				
Charge Termination Threshold	I _{term}	Charge current falls	6	11.2	16	%Icc
Recharge Threshold	•					
D 1	I_{RECH}	Charge current rises	25	33.3	42	%I _{CC}
Recharge Threshold	V _{RECH}	Battery voltage falls	4.02	4.085	4.15	V
Sleep Mode						
Sleep Mode Threshold	V _{SLP}	VIN from high to low, measures the voltage (VIN-V _{BAT})		10		mV
Sleep mode Release Threshold	V _{SLPR}	VIN from low to high, measures the voltage (VIN-V _{BAT})		60		mV
Over Charge Current Protection						
Over Charge Current Threshold	I _{OCP}		0.39	0.47	0.6	A
Deglitch Time	t _{OCP_DGL}	Deglitch time for over charge current protection	1.2	2	2.7	ms
ISET Pin						
ICET D' V 1		Precharge mode	0.32	0.4	0.48	T 7
ISET Pin Voltage		Constant current mode	1.12	1.205	1.29	V

Electrical Characteristics (Continued from last page)

Parameters	Symbol	Test Conditions	Min	Тур	Max	Unit
TEMP Pin						
TEMP Source Current	ITEMP		27	30	33	uA
Cold Threshold	V_{COLD}	V _{TEMP} rises, cool to cold	800	850	900	mV
Cold Release Threshold	V_{COLDR}	V _{TENP} falls, cold to cool	755	805	855	mV
Cool Threshold	V_{COOL}	V _{TEMP} rises, normal to cool	510	550	590	mV
Cool Release Threshold	V_{COOLR}	V _{TENP} falls, cool to normal	465	505	545	mV
Warm Threshold	V_{WARM}	V _{TENP} falls, normal to warm	120	135	150	mV
Warm Release Threshold	V_{WARMR}	V _{TEMP} rises, warm to normal	138	155	172	mV
Hot Threshold	V_{HOT}	V _{TENP} falls, warm to hot	85	100	115	mV
Hot Release Threshold	V_{HOTR}	V _{TEMP} rises, hot to warm	105	120	135	mV
CHRG Pin						
CHRG Sink Current	I _{CHRG}	V _{CHRG} =0.3V, Termination mode		10		mA
CHRG Leakage Current		V _{CHRG} =6V, Sleep mode			1	uA

Note: I_{CC} is the full-scale charge current set by the resistor at ISET pin.

Detailed Description

The CN3140 is a linear battery charger IC designed primarily for charging single cell lithium-ion or lithium-polymer battery. Featuring an internal P-channel power MOSFET, the charger uses a constant-current/constant-voltage to charge the battery. Continuous charge current can be set up to 400mA with an external resistor, and minimum charge current can be set down to 3mA, which makes CN3140 very suitable for ultra-small capacity battery. The on-chip adaptive cell can adjust charging current automatically based on the output capability of input power supply, so CN3140 is ideally suited for the solar-powered systems, or the applications that need to charge a battery with an input power supply whose output capability is limited. The open-drain output CHRG indicates the charger's status. The internal thermal regulation circuit reduces the programmed charge current if the die temperature attempts to rise above a preset value of approximately 132°C. This feature protects the CN3140 from excessive temperature, and allows the user to push the limits of the power handling capability of a given circuit board without risk of damaging the CN3140 or the external components. Another benefit of adopting thermal regulation is that charge current can be set according to typical, not worst-case, ambient temperatures for a given application with the assurance that the charger will automatically reduce the current in worst-case conditions.

The charge cycle begins when the voltage at the VIN pin rises above 4.5V and above battery voltage, a current set resistor is connected from the ISET pin to ground. The CHRG pin outputs a pulse of 2Hz frequency to indicate that the charge cycle is ongoing. At the beginning of the charge cycle, if the voltage at BAT pin is below 2.8V(Typical), the charger is in precharge mode to bring the cell voltage up to a safe level for charging. The charger goes into the fast charge constant-current mode once the voltage on BAT pin rises above 2.8V. In constant current mode, the charge current is set by RISET. When the battery approaches the regulation voltage of 4.2V, the charge current begins to decrease as the CN3140 enters the constant-voltage mode. When the current drops to charge termination threshold, CHRG pin output low level to indicate that the charge cycle is terminated. The charge termination threshold is 11.2% of the current in constant current mode. To restart the charge cycle, just remove the input voltage and reapply it. The charge cycle can also be automatically restarted if the charge current rises above the recharge threshold. The on-chip reference voltage, error amplifier and the resistor divider provide regulation voltage with 1% accuracy which can meet the requirement of lithium-ion and lithium polymer

batteries. When the input voltage is not present, the charger goes into a sleep mode, dropping battery drain current to less than 3uA. This greatly reduces the current drain on the battery and increases the standby time. During charging, battery temperature is continuously monitored in cooperation with an NTC thermistor connected between TEMP pin and GND. The battery temperature monitoring function provided by CN3140 is designed to follow JEITA temperature standard for lithium batteries.

The charging profile is shown in the following figure:

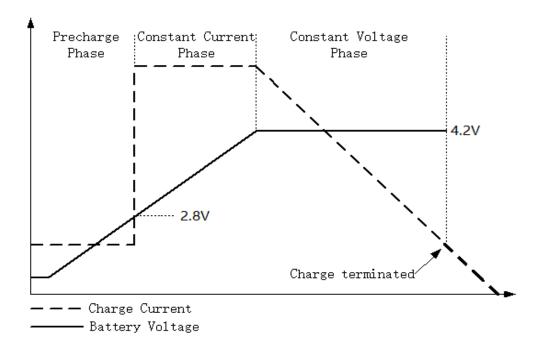


Figure 3 Charging Profile

Application Information

Undervoltage Lockout (UVLO)

An internal undervoltage lockout circuit monitors the input voltage and keeps the charger in shutdown mode until VIN rises above the undervoltage lockout voltage. The UVLO circuit has a built-in hysteresis of 0.12V.

Sleep mode

There is an on-chip sleep comparator. The comparator keeps the charger in sleep mode if VIN falls below sleep mode threshold (V_{BAT} +10mv). Once in sleep mode, the charger will not come out of sleep mode until VIN rises 60mv above the battery voltage.

Precharge mode

At the beginning of a charge cycle, if the battery voltage is below 2.8V(Typical), the charger goes into precharge mode or trickle charge mode, and the charge current is 33.3% of fast charge current in constant current mode.

Charging Current limited by the Output capability of Input Power Supply

If the output capability of input power supply is less than the charging current set by the resistor at ISET pin, then the on-chip adaptive cell will begin to function to adjust the charging current based on the output capability of input power supply. In this case, the actual charging current may be less than the value set by the resistor at ISET pin, but it is maximized to the output capability of input power supply on the condition that the input voltage at VIN pin is no less than 4.5V(Typical), which is the minimum operating voltage of CN3140. So the charging current can be set according to the maximum output capability of input power supply, not the worst case.

Setting Charge Current

The formula for the charge current in constant current mode is:

$$I_{CH} = 502V \ / \ R_{ISET}$$

Where:

ICH is the charge current in ampere

R_{ISET} is the total resistance from the ISET pin to ground in ohm

For example, if 400mA charge current is required, calculate:

$$R_{ISET} = 502V/0.4A = 1.25k\Omega$$

For best stability over temperature and time, 1% metal film resistors are recommended. If the charger is in constant-temperature or constant voltage mode, the charge current can be monitored by measuring the ISET pin voltage, and the charge current is calculated as the following equation:

$$I_{CH} = (V_{ISET} / R_{ISET}) \times 417$$

Combine Two Power Inputs

Although the CN3140 allows charging from a solar power supply, a wall adapter or a USB port can also be used to charge Li-Ion/Li-polymer batteries. Figure 4 shows an example of how to combine 2 power inputs. A P-channel MOSFET, M1, is used to prevent back conducting into the 2^{nd} power supply when the 1^{st} power supply is present and Schottky diode, D1, is used to prevent 2^{nd} power supply loss through the $1k\Omega$ pull-down resistor.

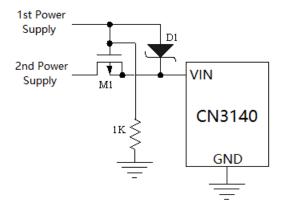


Figure 4 Combining 2 Input Power Supply

Battery Temperature Monitoring

To prevent the damage caused by the very high or very low temperature done to the battery, the CN3140 continuously monitors battery temperature by measuring the voltage at TEMP pin which is determined by TEMP pin's source current (30uA typical) and a negative temperature coefficient(NTC) thermistor connected between TEMP pin and GND as shown in Figure 1.

If battery temperature monitoring function is not needed, connect a fixed 10Kohm resistor from TEMP to GND. The battery temperature monitoring function for CN3140 is designed to follow the JEITA temperature standard for Li-Ion and Li-Poly batteries; charge current and regulation voltage (V_{REG}) are reduced based on battery temperature ranges.

There are totally five battery temperature ranges for CN3140:

• Hot: Above 55°C,

● Warm: 45°C to 55°C,

• Normal: 10°C to 45°C

• Cool: 0°C to 10°C,

● Cold: Below 0°C.

Normal operation occurs when battery temperature is between 10°C and 45°C, charge current and voltage will be the normal values.

When battery is in the Cool temperature range, which is between 0°C and 10°C, the charge current is 25% of full-scale charge current and regulation voltage is not changed.

When the battery is in the Warm temperature range, which is between 45°C and 55°C, the charge current is reduced to 50% of full-scale charge current and regulation voltage is reduced to 4.085V from 4.2V.

Charging is suspended if battery temperature is below Cold temp of 0°C or above Hot temp of 55°C. When charging is suspended, CHRG pin becomes high impedance state.

Once battery temperature is not in hot range and cold range, charging resumes automatically.

TEMP pin voltage is the product of its source current and NTC's resistance, so the selection of NTC should make sure TEMP pin voltage meet the requirements of hot, warm, cool and cold threshold. An NTC of nominal resistance 10K ohm at room temperature may be suitable for CN3140.

A resistor of small resistance in series with NTC thermistor and a resistor of large resistance in parallel with NTC thermistor can fine tune CN3140's temperature range.

The following table lists TEMP pin voltage, charge current and regulation voltage in the above-mentioned 5 battery temperature range.

Battery Temperature Range	TEMP Pin Voltage	Charge Current	Regulation Voltage
Hot: above 55°C	$V_{TEMP} < V_{HOT}$	Charge Suspended	Charge Suspended
Warm: 45°C to 55°C	$V_{HOTR} < V_{TEMP} < V_{WARM}$	50%*I _{CC}	4.085V
Normal: 10°C to 45°C	$V_{WARMR} < V_{TEMP} < V_{COOLR}$	Normal Value	4.2V
Cool: 0°C to 10°C	$V_{COOL} < V_{TEMP} < V_{COLDR}$	25%*I _{CC}	4.2V
Cold: below 0°C	$V_{TEMP} > V_{COLD}$	Charge Suspended	Charge Suspended

Recharge

After a charge cycle has terminated, if the charge current rises above 30% of the constant charge current, or battery voltage falls below 4.085V, a new charge cycle will begin automatically.

Constant-Current/Constant-Voltage/Constant-Temperature

The CN3140 use a unique architecture to charge a battery in a constant-current, constant-voltage, constant temperature fashion as shown in Figure 2. Amplifiers Iamp, Vamp, and Tamp are used in three separate feedback loops to force the charger into constant-current, constant-voltage, or constant-temperature mode, respectively. In constant current mode the charge current delivered to the battery equal to 502V/R_{ISET}. If the power dissipation of the CN3140 results in the junction temperature approaching 132 ℃, the amplifier Tamp will begin decreasing the charge current to limit the die temperature to approximately 132 ℃. As the battery voltage rises, the CN3140 either returns to constant-current mode or it enters constant voltage mode straight from constant-temperature mode.

Over Charge Current Protection

During normal operation, the charge current should be regulated to the value set by the resistor at ISET pin. However, if a short circuit or a misuse of abnormally small resistance occurs on ISET pin, the charge current may rise to unintended level. If the charge current exceeds over charge current threshold I_{OCP} , the CN3140 is turned off after a deglitch time of $t_{OCP\ DGL}$.

The CN3140 remains latched off, and will not restart operation until the input supply is removed and re-applied.

Open-Drain Status Outputs

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If battery temperature is in hot and cold range, CHRG pin becomes high impedance.

If CN3140 is in sleep mode, CHRG pin also becomes high impedance.

CHRG pin should be tied to ground if not used.

VIN Bypass Capacitor

Many types of capacitors can be used for input bypassing(C1 in Figure 1), Generally, a 1uF to 10uF (Case:0805) ceramic capacitor, placed in close proximity to VIN and GND pins, works well. In some applications depending on the input power supply characteristics, cable length or input power supply being short-circuit, it may be necessary to increase the capacitor's value.

If the ceramic capacitor is used as the input supply bypassing purpose, a voltage spike may be created when the input voltage is applied to the CN3140 via a cable. In extreme cases, the circuit shown in Figure 5 should be considered to use to prevent the CN3140 from being damaged by the voltage spike.

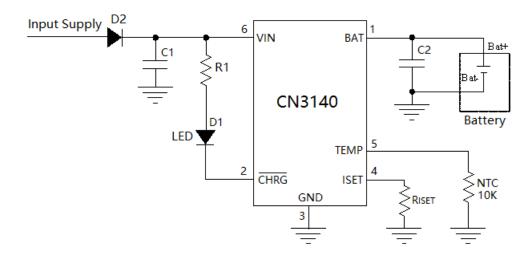


Figure 5 Adding Diode D2 to Suppress Voltage Spike

Stability

A capacitor from BAT pin to GND is required to stabilize the feedback loop, generally the feedback loop is stable with an 1uF to 22uF ceramic capacitor. If electrolytic capacitor is used, the capacitance can be as high as 100uF.

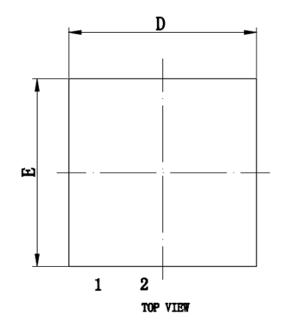
In constant current mode, the stability is also affected by the impedance at the ISET pin. With no additional capacitance on the ISET pin, the loop is stable with current set resistors values as high as $168K\,\Omega$. However, additional capacitance on ISET pin reduces the maximum allowed current set resistor, there should not be a capacitance between ISET pin and GND.

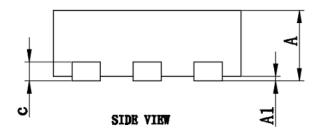
Board Layout Considerations

- 1. RISET at ISET pin should be as close to CN3140 as possible, also the parasitic capacitance at ISET pin should be kept as small as possible.
- 2. The capacitor at VIN pin and BAT pin should be as close to CN3140 as possible.
- 3. During charging, CN3140's temperature may be high, the NTC thermistor should be placed far enough to CN3140 so that the thermistor can reflect the battery's temperature correctly.
- 4. It is very important to use a good thermal PC board layout to maximize charging current. The thermal path for the heat generated by the IC is from the die to the copper lead frame through the package lead(especially the ground lead) to the PC board copper, the PC board copper is the heat sink. The footprint copper pads should be as wide as possible and expand out to larger copper areas to spread and dissipate the heat to the surrounding ambient. Feedthrough vias to inner or backside copper layers are also useful in improving the

overall thermal performance of the charger. Other heat sources on the board, not related to the charger, must also be considered when designing a PC board layout because they will affect overall temperature rise and the maximum charge current.

Package Information (DFN-6)



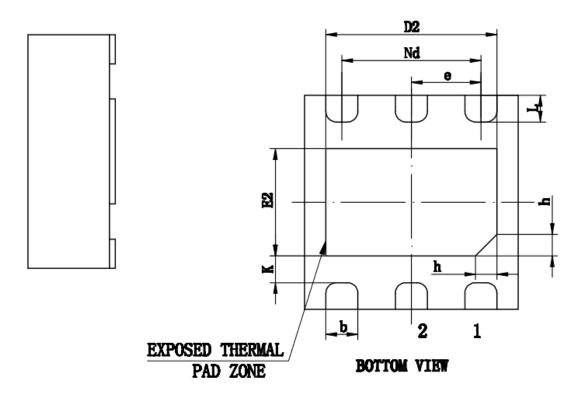


SYMBOL	MIN	NOM	MAX
A	0.7	0.75	0.8
A1	-	0.02	0.05
ъ	0.25	0.30	0.35
С	0.18	0.2	0.25
D	1.90	2.00	2.10
D2	1.5	1.6	1.7
е	0.65BSC 1.30BSC		
Nd			
E	1.90	2.00	2.10
E2	0.90	1.00	1.10
K	0.20	-	-
L	0.20	0.25	0.30
Li	0.15	0.20	0.25
L/F载体尺寸	69*47		

NOTES:

1.All DIMENSIONS REFER TO JEDEC STANDARD MO-229.

2.DIMENSION D DOES NOT INCLUDE MOLD FLASH
3.DIMENSION E1 DOSE NOT INCLUDE MOLD FLASH
4.FLASH OR PROTRUSION SHALL NOT EXCHED
0.25mm PER SIDE.



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